

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Orly Yadid-Pecht

Application No.:

10/053,130

U.S. Filing Date:

November 13, 2001

Title:

A METHOD AND APPARATUS FOR PROVIDING ADAPTIVE

MULTIPLE RESOLUTION OF OPTICAL IMAGERS

Group Art Unit:

2621

Examiner:

Couso, Jose L.

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 RECEIVED

MAY 2,3 2003

**Technology Center 2600** 

## INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for the Applicants hereby bring the following references to the examination of the above-identified application to the attention of the Examiner. The references are listed on the attached modified PTO/SB08B form. It is respectfully requested that the information be expressly considered during the prosecution of this application and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This statement is being filed under 37 C.F.R. § 1.97(b)(3) before the mailing of a first Office Action on the merits. Copies of the references are enclosed.

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited in the United States Postal Service as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 19, 2003.

Eunhee Park (Reg. No. 42,976)

No fee is believed necessary with the filing of these documents. However, if a fee is deemed necessary, the Commissioner for Patents is hereby authorized to charge the fee set forth in § 1.17(p) or credit any overpayments that may be necessary to Deposit Account No. 02-0393 of Baker & McKenzie.

Dated: May 19, 2003

Respectfully submitted,

**Eunhee Park** 

Reg. No. 42,976

Baker & McKenzie

805 Third Avenue

New York, New York 10022

Telephone (212) 751-5700

Facsimile (212) 759-9133

PTO/SB/08B (10-01)

PTO/SB/08B (10-01)
Approved for use through 10/31/2002. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
U.S. Patent and Trademark Office of the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB

Complete if Known Substitute for form 1449B/PTO 10/053.130 **Application Number** INFORMATION DISCLOSURE 11/13/2001 Orly Yadid-Pecht Filing Date STATEMENT BY APPLICANT First Named Inventor Group Art Unit 2621 Couso, Examiner Name Jose L (use as many sheets as necessary) of Attorney Docket Number 56156628-2 Sheet

Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s) publisher, city and/or country where published	T <sup>2</sup>
	1	KEMENY, S., et al; "Multiresolution Image Sensor"; Systems for Video Technology; Vol. 7, No. 4; August 1997; pp. 575-582	
	2	STROBACH, P.; "Quadtree-Structured Interframe Coding of HDTV Sequences"; SPIE Vol. 1001 Visual Communications and Image Processing '88; November, 1988; pp. 812-819	
	3	DICKINSON, A., et al; "A 256x256 CMOS Active Pixel Image Sensor with Motion Detection"; 1995 IEEE International Solid-State Circuits Conference; February 16, 1995; pp. 226-227	
	4	XIA, S., et al; "An All CMOS Foveal Image Sensor Chip"; IEEE International Asic Conference and Exhibit; 1998; pp. 409-413	
	5	AIZAWA, K., et al; "A Novel Image Sensor for Video Compression"; Proceedings Icip-94: Austin Convention Center Austin, Texas; November 13, 1994; pp. 591-595	
	6	PANICACCI, R., et al; "Programmable multiresolution CMOS active pixel sensor"; SPIE Vol. 2654 Proceedings, Intl. Conf. on Image Processing; Vol. II of III; October 26-29, 1997; pp. 72-79	
	7	PARDO, F., et al; "Space-Variant Nonorthogonal Structure CMOS Image Sensor Design"; IEEE Journal of Solid-State Circuits; Vol. 33, No. 6; June 1998; pp. 842-849	
······································	8	AIZAWA, K., et al; "Implementations of On Sensor Image Compression and Comparisons Between"; Proceedings, 1st. Intl. Conf. on Image Processing; October 26-29,1997; pp. 258-261	
	9	KAWAHITO, S., et al; "A CMOS Image Sensor with Analog Two-Dimensional DCT-Based Compression"; IEEE Journal of Solid-State Circuits; Vol. 32, No. 12; Dec. 1997; pp. 2030-2041	
		RECEIVED	•
		MAY 2 3 2003	-
		Technology Center 2	26

Examiner	Date
Signature	Considered

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.